

[DRIVING CIRCUIT OF DISPLAY AND FLAT PANEL DISPLAY]

Abstract

A driving circuit and a flat panel display using thereof are disclosed. The driving circuit includes a gamma voltage generator, a plurality of first buffers and a converter. The gamma voltage generator is used for providing a plurality of first gamma voltages. Each of the first buffers is provided for receiving each of the first gamma voltages to generate a second gamma voltage respectively. The converter is provided for receiving the second gamma voltages and the first display data to output the image driving signal by selecting one of the corresponding second gamma voltages according to the first display data.